



Attorney's Docket No. 4640.P006

PATENT

2133  
#7  
SP  
7-22-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of: )  
Yervant Zorian )  
Application No.: 10/083,241 )  
Filed: February 25, 2002 )  
For: APPARATUS AND METHOD TO )  
GENERATE A REPAIR )  
SIGNATURE )

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Examiner: Unassigned

Art Unit: 2133

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Technology Center 2100

Commissioner for Patents  
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Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

A statement pursuant to 37 C.F.R. §1.97(e) or

A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7-11, 2003

  
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<b>Examiner Signature</b>		<b>Date Considered</b>	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language translation is attached.

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Substitute for Form 1449/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/083,241
				Filing Date	February 25, 2002
				First Named Inventor:	Yervant Zorian
				Art Unit	2133
				Examiner Name	Unassigned
Sh et	2	of	2	Attorney Docket Number	04640.P006
<b>NON PATENT LITERATURE DOCUMENTS</b>					
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			
		Yervant Zorian: "System-Chip Test Strategies" (Tutorial): DAC 1998: 752-757.			
		Ilyoung Kim, Yervant Zorian, Goh Komoriya, Hai Pham, Frank P. Higgins, Jim L. Lewandowski: " Built-in Self-Repair for Embedded High Density SRAM." ITC 1998: 1112-1119.			
		Yervant Zorian, Erik Jan Marinissen, Sujit Dey: "Testing Embedded-Core Based System Chips." ITC 1998: pages 1-14.			
		Yervant Zorian, Erik Jan Marinissen, "System Chip Test: How Will It Impact Your Design?" pages 1-6, DAC, 2000.			
		Monica Lobetti Bodoni, Alfredo Benso, Silvia Chiusano, Stefano DiCarlo, Giorgio DiNatale, Paolo Prinetto, "An Effective Distributed BIST Architecture for RAMs," pages 1-6, IEEE European Test Workshop, 2000.			
		Chauchin Su, Shih-Ching Hsiao, Hau-Zen Zhai, Chung-Len Lee, "A Computer Aided Engineering System for Memory BIST," pages 1-4, DAC, 2001.			
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